

**IN THE CLAIMS:**

Please cancel claims 16 and 32 without prejudice or disclaimer as to the subject matter contained therein.

Please amend the claims as shown in the following claims listing.

1. (Currently amended) A system, comprising:  
a plurality of nodes, wherein each node of the plurality of nodes includes a plurality of active devices ~~coupled by~~ and an address network that is configured to couple the plurality of active devices together; and  
an inter-node network coupled to the address network and configured to convey communications between the plurality of nodes;  
wherein [an] each address network ~~included in a node of the plurality of nodes~~ is independently configured to convey address packets specifying a coherency unit [in] using a broadcast mode[;]  
~~wherein a different address network included in a different node of the plurality of nodes is configured to convey address packets specifying the coherency unit in~~ and a point-to-point mode; and  
wherein each address network is further configured to independently select one of the broadcast mode and the point-to-point mode dependent upon whether the coherency unit maps to any of the active devices of the plurality of active devices included in the node.
2. (Currently amended) The system of claim 1, wherein the plurality of active devices includes one or more memory subsystems, and wherein in response to determining the coherency unit does not map to any of the one or more memory subsystems included in the node, and wherein the address network in the node is configured to convey all address packets specifying ~~non-mapped the coherency units~~ unit in the broadcast mode.

3. (Currently amended) The system of claim 2, wherein [the] each node of the plurality of nodes includes an interface coupled to the address network, wherein the interface is [and] configured to send and receive coherency messages on the inter-node network, wherein the interface is configured to store a record for each of the address packets specifying the coherency unit in an outstanding transaction queue, and wherein the interface is configured to send a coherency message specifying the coherency unit via the inter-node network to a home node for the coherency unit in response to [each] the record being placed in the outstanding transaction queue.

4. (Currently amended) The system of claim 1, wherein ~~the~~ each address network is independently configured to convey all address packets only in broadcast mode.

5. (Currently amended) The system of claim 1, wherein ~~the additional~~ each address network is independently configured to convey all address packets only in point-to-point mode.

6. (Currently amended) The system of claim [1] 2, wherein in response to determining the coherency unit does map to any of the one or more memory subsystems included in the node, the ~~additional~~ address network is configured to convey all address packets specifying the coherency units unit that map to a memory subsystem included in the node in the point-to-point mode.

7. (Currently amended) The system of claim 1, wherein each node includes a transmission mode unit configured to store an indication ~~to control~~ that controls whether a given address packet is transmitted through the address network in the broadcast mode or the point-to-point mode.

8. (Currently amended) The system of claim 7, wherein the transmission mode unit included in [the] each node is configured to store a plurality of additional indications to control whether address packets other than the given address packet are transmitted through the address network in point-to-point mode or in broadcast mode.

9. (Currently amended) The system of claim 7, wherein the transmission mode unit is configured to dynamically update the indication to ~~indicate that~~ switch a conveyance mode of address packets specifying a given coherency unit ~~should be conveyed by the address network in a different mode than such address packets were previously conveyed from the point-to-point mode to the broadcast mode, and vice versa.~~

10. (Currently amended) The system of claim 1, wherein ~~the~~ each node further includes a respective directory, wherein the directory includes a plurality of entries, each entry corresponding to a different coherency ~~units~~ unit that is mapped to a memory subsystem included in the node, wherein each entry contains an indication of whether a cached copy of a corresponding coherency unit has been created in one or more of the plurality of active devices included in the node.

11. (Currently amended) The system of claim 10, wherein ~~when an~~ address packets [is] conveyed by the address network in broadcast mode, ~~the address packet is~~ are broadcast to each of the plurality of active devices included in the node regardless of information contained within the directory.

12. (Currently amended) The system of claim 10, wherein ~~when the~~ address packets [is] conveyed by the address network in point-to-point mode, ~~the address packet is~~ are conveyed by the address network to a memory subsystem included in the node; and

wherein the memory subsystem is configured to access the directory and  
responsively send address packets to one or more of the plurality of active  
devices dependent upon information contained within the directory.

13. (Currently amended) The system of claim 1, wherein address packets specifying some coherency units are transmitted through the address network in the point-to-point mode while address packets specifying other coherency units are transmitted through the address network in the broadcast mode.

14. (Currently amended) The system of claim 10, wherein the address network is further configured to select whether to transmit [an] address packets in the broadcast mode or in the point-to-point mode dependent on an address specified in each of the address packets.

15. (Currently amended) The system of claim 1, wherein one of the plurality of active devices included in the node is configured to transition an ownership responsibility for the coherency unit at a different time than an access right [to] associated with the coherency unit transitions.

16. (Cancelled)

17. (Currently amended) A method of operating a multi-node computer system, wherein the multi-node computer system includes a plurality of nodes coupled together by an inter-node network, wherein each node includes a plurality of active devices and an address network coupling together the plurality of active devices, the method comprising:

[an] each address network ~~included in a node of the plurality of nodes~~  
independently conveying address packets specifying a coherency unit [in]  
using a broadcast mode[;]  
~~an the address network included in a different node of the plurality of nodes~~  
~~conveying address packets specifying the coherency unit in and a~~ point-to-  
point mode; and  
each address network independently selecting one of the broadcast mode and the  
point-to-point mode dependent upon whether the coherency unit maps to  
any of the active devices of the plurality of active devices included in the  
node.

18. (Currently amended) The method of claim 17, wherein the plurality of active devices includes one or more memory subsystems, the method further comprising the

address network in the node conveying all address packets specifying ~~non-mapped~~ the coherency ~~units~~ unit in the broadcast mode, ~~wherein in response to determining the~~ coherency unit does not map to any of the one or more memory subsystems included in the node.

19. (Currently amended) The method of claim 18, further comprising:  
an interface included in [the] each node and coupled to the address network  
storing a record for each of the address packets specifying the coherency  
unit in an outstanding transaction queue; and  
the interface sending a coherency message specifying the coherency unit via the  
inter-node network to a home node for the coherency unit in response to  
[each] the record being placed in the outstanding transaction queue.
20. (Currently amended) The method of claim 17, further comprising ~~the~~ each address  
network independently conveying all address packets only in the broadcast mode.
21. (Currently amended) The method of claim 17, further comprising ~~the additional~~  
each address network independently conveying all address packets only in the point-to-  
point mode.
22. (Currently amended) The method of claim [17] 18, further comprising ~~the~~  
additional in response to determining the coherency unit does map to any of the one or  
more memory subsystems included in the node, each address network conveying all  
address packets specifying the coherency units unit ~~that map to a memory subsystem~~  
~~included in the node~~ in the point-to-point mode.
23. (Currently amended) The method of claim 17, further comprising a transmission  
mode unit included in the node storing an indication ~~to control~~ that controls whether a  
given address packet is transmitted through the address network in the broadcast mode or  
the point-to-point mode.

24. (Currently amended) The method of claim 23, further comprising the transmission mode unit included in [the] each node storing a plurality of additional indications to control whether address packets other than the given address packet are transmitted through the address network in point-to-point mode or in broadcast mode.

25. (Currently amended) The method of claim 23, further comprising the transmission mode unit dynamically updating the indication to ~~indicate that~~ switch a conveyance mode of address packets specifying a given coherency unit ~~should be conveyed by the address network in a different mode than such address packets were previously conveyed from the point-to-point mode to the broadcast mode, and vice versa.~~

26. (Currently amended) The method of claim [17] 18, wherein ~~the~~ each node further includes a directory, wherein the directory includes a plurality of entries, each entry corresponding to a different coherency unit that is mapped to [a] one or more of the memory subsystems included in the node, wherein each entry contains an indication of whether a cached copy of a corresponding coherency unit has been created in one or more of the plurality of active devices included in the node.

27. (Currently amended) The method of claim 26, ~~further comprising broadcasting~~ wherein conveying [an] address packets ~~to each of the plurality of active devices included in the node [when] the address packets [is] are conveyed by the address network in the~~ broadcast mode includes conveying the address packets to each of the plurality of active devices included in the node regardless of information contained within the directory.

28. (Currently amended) The method of claim 26, further comprising the address network conveying an address packet to [a] at least one of the memory subsystems included in the node when the address packet is conveyed by the address network in the point-to-point mode; and

in response to receiving the address packet, the at least one of the memory subsystems accessing the directory and responsively sending

corresponding address packets to one or more of the plurality of active devices dependent upon information contained within the directory.

29. (Currently amended) The method of claim 17, further comprising the address network conveying address packets specifying some coherency units in the point-to-point mode and conveying address packets specifying other coherency units in the broadcast mode.

30. (Currently amended) The method of claim 29, further comprising the address network selecting whether to transmit an address packet in the broadcast mode or in the point-to-point mode dependent on an address specified in the address packet.

31. (Currently amended) The method of claim 17, ~~wherein~~ further comprising at least one of the plurality of active devices included in the node ~~is configured to transition~~ transitioning an ownership responsibility for the coherency unit at a different time than an access right [to] associated with the coherency unit transitions.

32. (Cancelled)